

**REMARKS**

At the outset, the Examiner is thanked for the thorough review and consideration of the subject application. The Non-Final Office Action of June 18, 2003 has been received and its contents carefully reviewed.

By the present amendment, Applicants hereby amend claims 1, 14, and 16-21 and add new claims 22-24. Applicants respectfully submit no new matter has been introduced by the present amendment.

The Examiner rejected claim 1 under 35 U.S.C. § 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention; and rejected claims 1-21 are rejected under 35 U.S.C. § 103(a) as being unpatentable over the related art illustrated in Figures 3 and 4 in view of Ito et al. (U.S. Pat. No. 5,748,179) and Suzuki et al. (U.S. Pat. No. 5,739,880). These rejections are traversed and reconsideration of the claims is respectfully requested in view of the following amendments and remarks.

The rejection of claim 1 under 35 U.S.C. § 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention, is respectfully traversed and reconsideration is requested.

In rejecting claim 1 under 35 U.S.C. § 112, second paragraph, the Examiner stated the “recitation ‘...wherein one of the gate transmitting line has a resistance of below 30 Ω and is capable of transmitting a gate low voltage.’ is indefinite. Because the gate transmitting line is capable of transmitting a gate low voltage that indicates the gate transmitting line can or may transmit a gate low voltage, but it does not indicate the definite function of the gate transmitting line.”

Applicants respectfully submit, however, the rejection of claim 1 under 35 U.S.C. § 112, second paragraph, is rendered moot in view of the aforementioned amendment to claim 1 above.

The rejection of claims 1-21 are rejected under 35 U.S.C. § 103(a) as being unpatentable over the related art illustrated in Figures 3 and 4 in view of Ito et al. and Suzuki et al. is respectfully traversed and reconsideration is requested.

Claim 1 is allowable over the cited references in that claim 1 recites a combination of elements including, at least “a plurality of gate transmitting lines connecting the gate pads with the source pads, the plurality of gate transmitting lines transmitting signals from the first printed circuit board to the second printed circuit board via the gate transmitting lines, wherein a first gate transmitting line of the plurality of gate transmitting lines has a first resistance, wherein gate transmitting lines other than the first gate transmitting line have a second resistance, and wherein the first resistance is less than the second resistance.” None of the cited references, including the related art illustrated in Figures 3 and 4, Ito et al., or Suzuki et al., singly or in combination, teach or suggest at least these features of the claimed invention. Accordingly, Applicants respectfully submit that claims 2-13 and 22-24, which depend from claim 1, are also allowable over the cited references.

Claim 14 is allowable over the cited references in that claim 14 recites a combination of elements including, for example “a plurality of gate transmitting lines formed directly on the substrate and connecting the gate pads with the source pads, the plurality of gate transmitting lines transmitting signals from the first printed circuit board to the second printed circuit board via the gate transmitting lines, wherein the plurality of gate transmitting lines comprise two resistances.” None of the cited references, including the related art illustrated in Figures 3 and 4, Ito et al., or Suzuki et al., singly or in combination, teach or suggest at least these features of the

claimed invention. Accordingly, Applicants respectfully submit that claims 15-19, which depend from claim 14, are also allowable over the cited references.

Claim 20 is allowable over the cited references in that claim 20 recites a combination of elements including, for example “forming a plurality of gate transmitting lines formed directly on the substrate and connecting the gate pads with the source pads, the plurality of gate transmitting lines transmitting signals from the first printed circuit board to the second printed circuit board via the gate transmitting lines, wherein the plurality of gate transmitting lines comprise two resistances.” None of the cited references, including the related art illustrated in Figures 3 and 4, Ito et al., or Suzuki et al., singly or in combination, teach or suggest at least these features of the claimed invention. Accordingly, Applicants respectfully submit that claim 21, which depends from claim 20, is also allowable over the cited references.

Further, the Examiner cites the related art illustrated in Figures 3 and 4 as “Applicant admitted prior art (AAPA).” Applicants respectfully submit, however, that the present application does not make any admission that the concepts related to Figures 1-6 are “prior art”. Rather, the concepts related to Figures 1-6 were provided merely for illustrative purposes. Accordingly, Applicants respectfully submit the concepts disclosed in Figures 1-6 cannot be used in a rejection under 35 U.S.C. § 103(a) as they do not constitute “prior art”. As no admission regarding the availability of the concepts related to Figures 1-6 as “prior art” have been made, Applicants respectfully request the rejection under 35 U.S.C. § 103(a), of which the related art illustrated in Figures 3 and 4 was made a part, be withdrawn.

Applicants believe the foregoing amendments and remarks place the application in condition for allowance and early, favorable action is respectfully solicited. Should the

Examiner deem that a telephone conference would further the prosecution of this application, the Examiner is invited to call the undersigned attorney at (202) 496-7500.

If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136. Please credit any overpayment to deposit Account No. 50-0911.

Respectfully submitted,

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Date: September 17, 2003

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